

## REMARKS

### Status of the Claims

Claims 1, 17, 33, 42, 48, 53 and 59 have been amended to clarify the claimed invention. Claims 1-63 are pending in the case.

### Withdrawal of Election/Restriction Requirement in Prior Action

The previous Action asserted an election/restriction requirement, and in its prior response, Applicant traversed that requirement. Because all claims were examined in the recently received Office Action, Applicant assumes that this election/restriction requirement has been withdrawn. Applicant respectfully acknowledges this decision.

### Rejection of the Pending Claims

The Office Action has rejected the pending claims under §102 and §103 using various combinations that rely primarily upon U.S. Patent Application Publication No. 2002/0013133 A1 ("Lam"). Applicant traverses these rejections.

Lam discloses analog-to-digital converters (ADCs) and digital-to-analog converters (DACs) with input signals that are first passed through digital delay blocks (*see e.g.*, FIGS. 6-7). However, in contrast to the present invention as described below, Lam does not disclose ADCs and DACs that are controlled by sampling or operational clock signals that are processed through delay blocks. Rather, the circuitry of Lam is similar to the prior art depicted in FIG. 1 of the present Application.

The pending claims distinguish from prior systems, such as Lam. Rather than rely upon the prior art method of adjusting the outputs of the ADC and DAC with a programmable delay, as shown in FIG. 1 (Prior Art), the claims require that the sampling clock signal that controls the ADC in the receive path circuitry and the clock signal that controls the operational rate of the DAC in the transmit path circuitry be adjusted by a programmable time delay. [*See*, FIGS. 3A-3C and associated text.] Thus, the outputs of the ADCs and the DACs are controlled through the programmable delay adjustments to the sampling and operational clock signals that control the ADC and DAC without the necessity of adding delay blocks to further process those outputs. [*See*, Application page 12, lines 11-18.]

The pending claims, therefore, are not anticipated or made obvious by the cited references. Applicant respectfully request withdrawal of the pending rejections.

Conclusion

Applicant respectfully requests reconsideration of the Application.

The Examiner is invited to contact the undersigned at the phone number indicated below with any questions or comments or to otherwise facilitate expeditious and compact prosecution of the application.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Brian W. Peterman", is written over a horizontal line.

Brian W. Peterman  
Registration No. 37,908  
Attorney for Applicant

O'KEEFE, EGAN & PETERMAN  
1101 Capital of Texas Highway South  
Building C, Suite 200  
Austin, Texas 78746  
(512) 347-1611  
FAX: (512) 347-1615